FIG.1

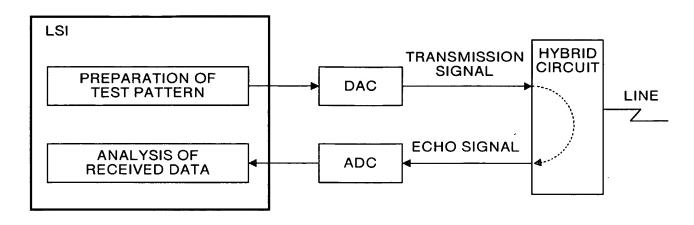
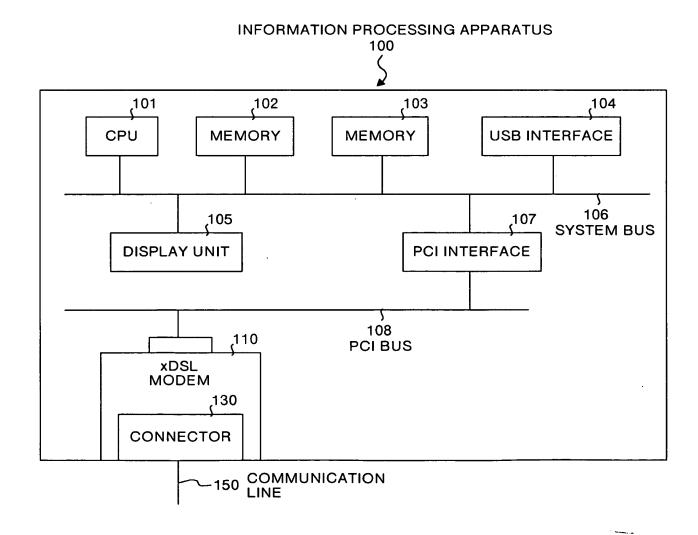


FIG.2



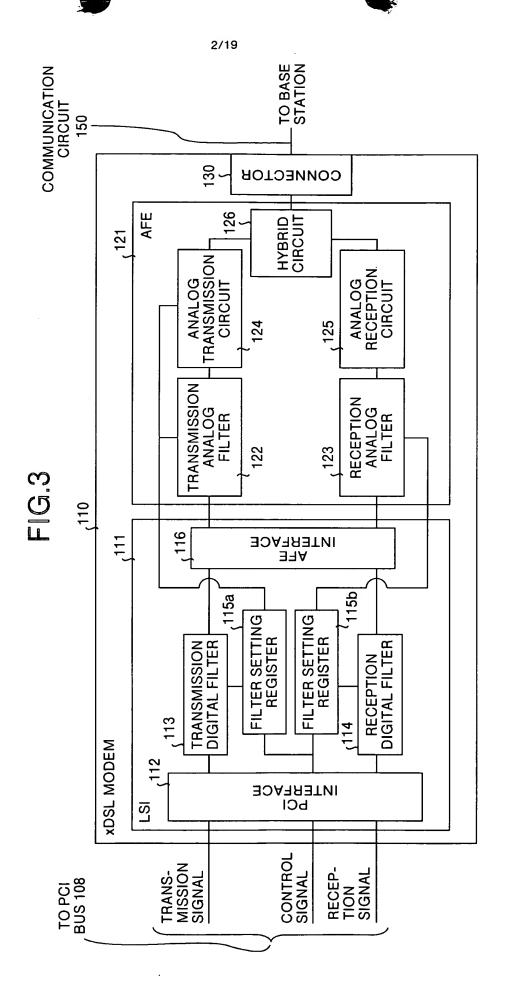


FIG.4

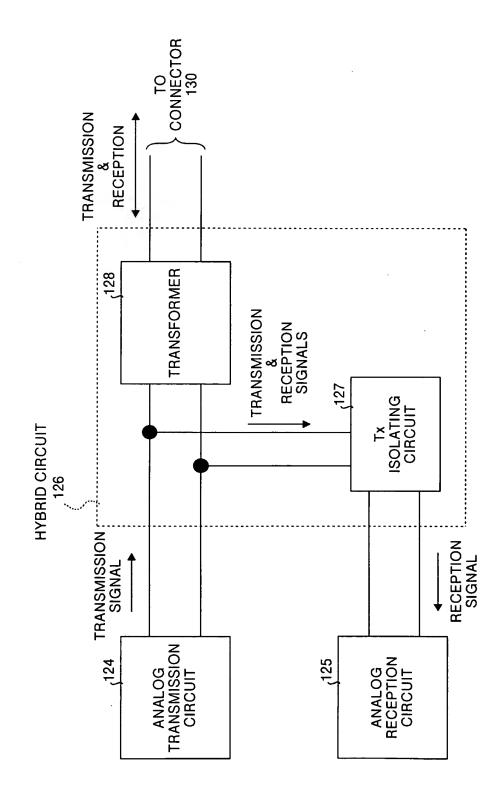
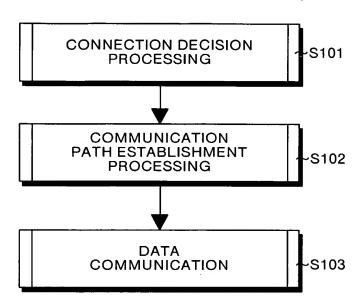


FIG.5



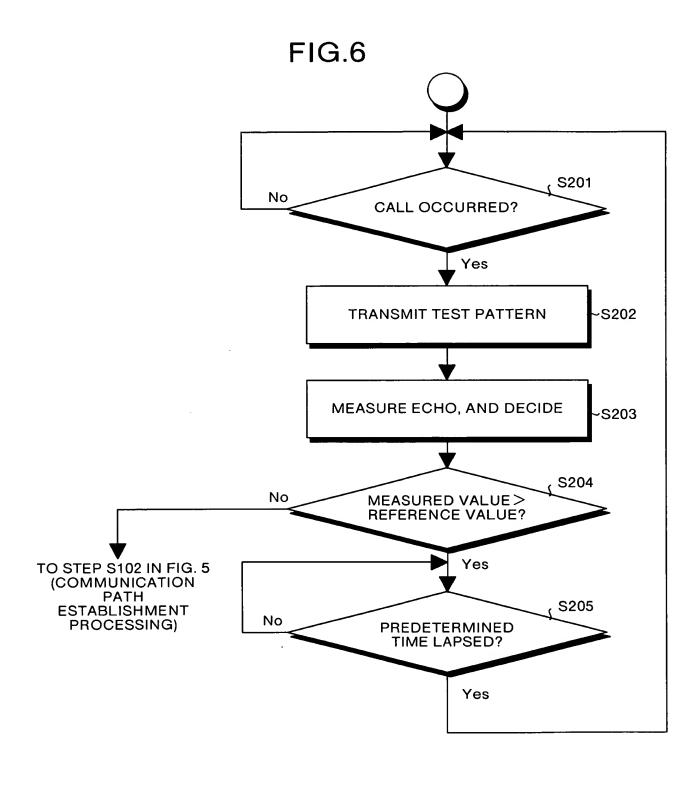
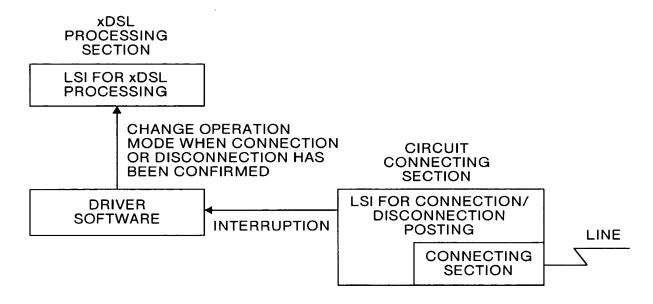
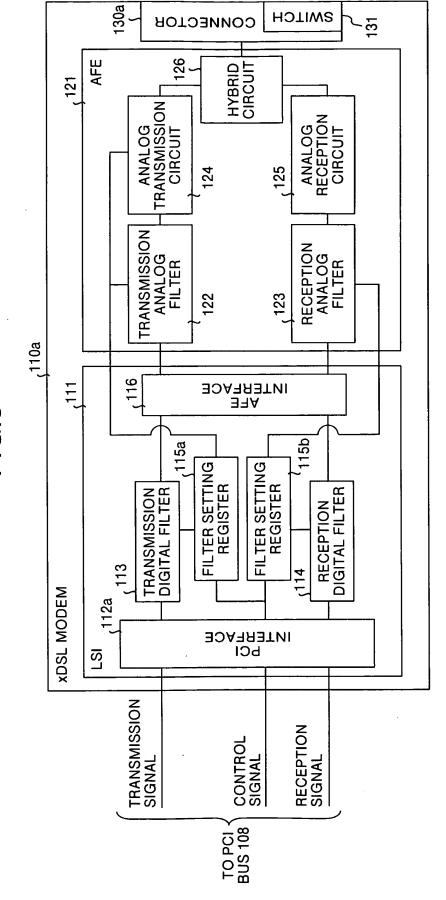


FIG.7





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FIG.9

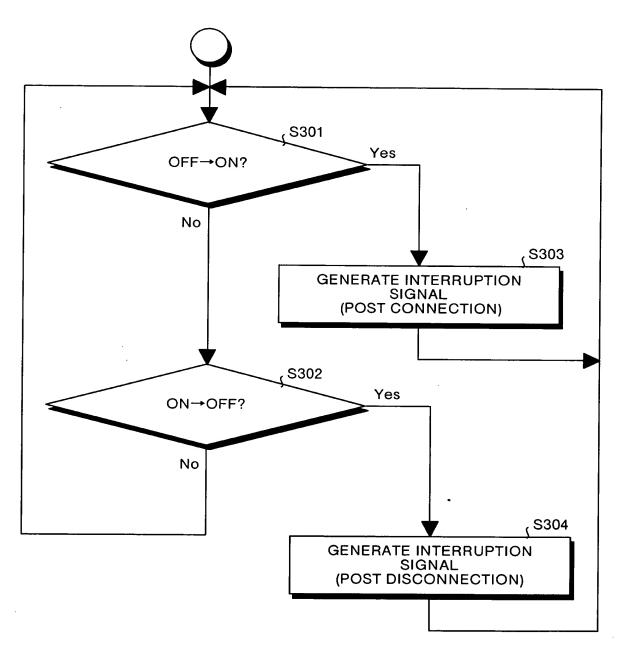


FIG.10

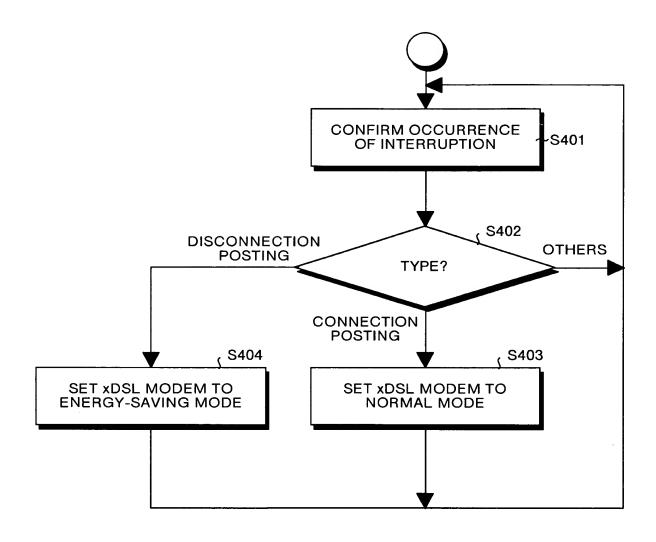
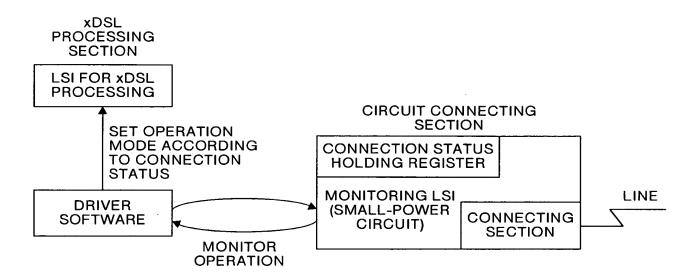


FIG.11



CONNECTOR

SWITCH

131

130a

AFE 126 HYBRID CIRCUIT 121 ANALOG TRANSMISSION -CIRCUIT ANALOG RECEPTION CIRCUIT 125 124 TRANSMISSION ANALOG FILTER RECEPTION ANALOG FILTER 122 , 123 ,110b 116 Ξ AFE INTERFACE (115b ,115a FILTER SETTING REGISTER FILTER SETTING REGISTER TRANSMISSION DIGITAL FILTER RECEPTION DIGITAL FILTER 113 (114 135 *xDSL MODEM* ,112b PCI INTERFACE REGISTER <u>LSI</u> TRANSMISSION SIGNAL RECEPTION SIGNAL CONTROL SIGNAL TO PCI <

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FIG.13

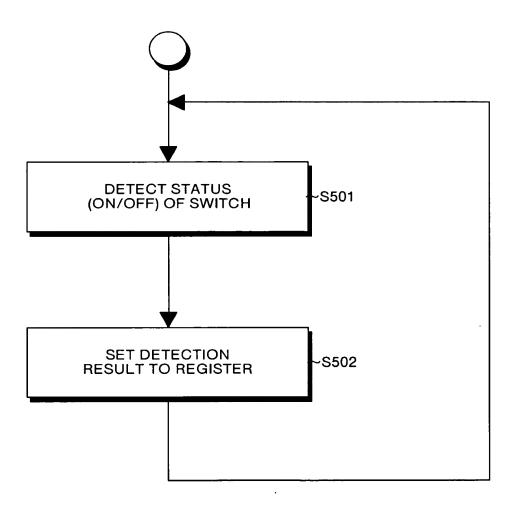


FIG.14

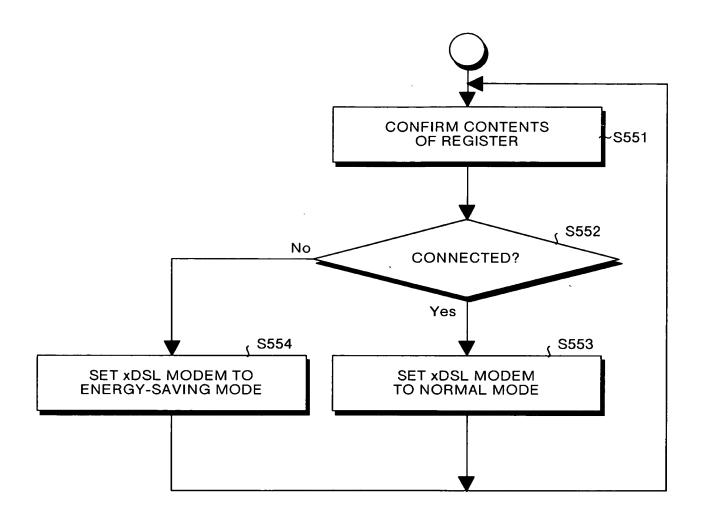


FIG.15

		STATUS OF POWER SOURCE	
		EXTERNAL POWER MODE	INTERNAL POWER SOURCE MODE
ERROR CONTENTS	ERROR IN SOFTWARE PROCESSING	L TIMES	M TIMES
	NO RESPONSE FROM THE OTHER PARTY	L TIMES	N TIMES

WHERE N<M<L

FIG.16

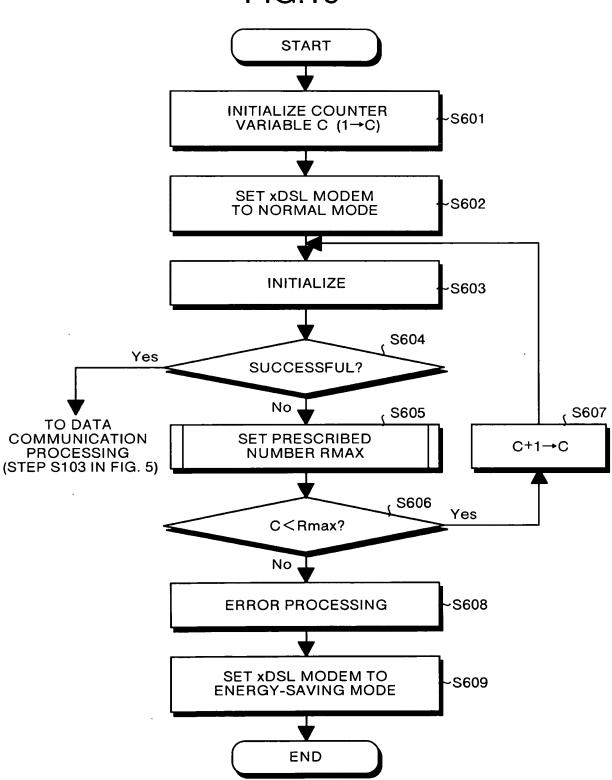


FIG.17

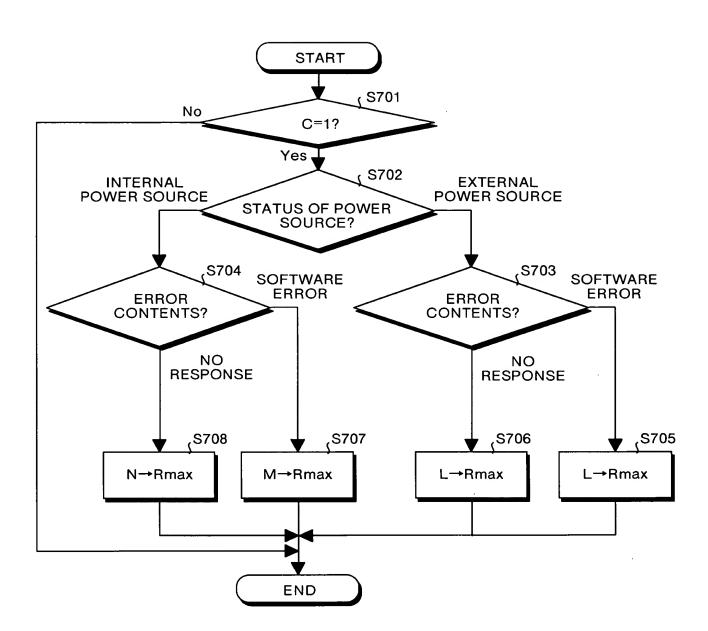


FIG.18

		STATUS OF POWER SOURCE	
		EXTERNAL POWER MODE	INTERNAL POWER SOURCE MODE
ERROR CONTENTS	ERROR IN SOFTWARE PROCESSING	t1	t2
	NO RESPONSE FROM THE OTHERPARTY	t2	t2

WHERE t1 < t2

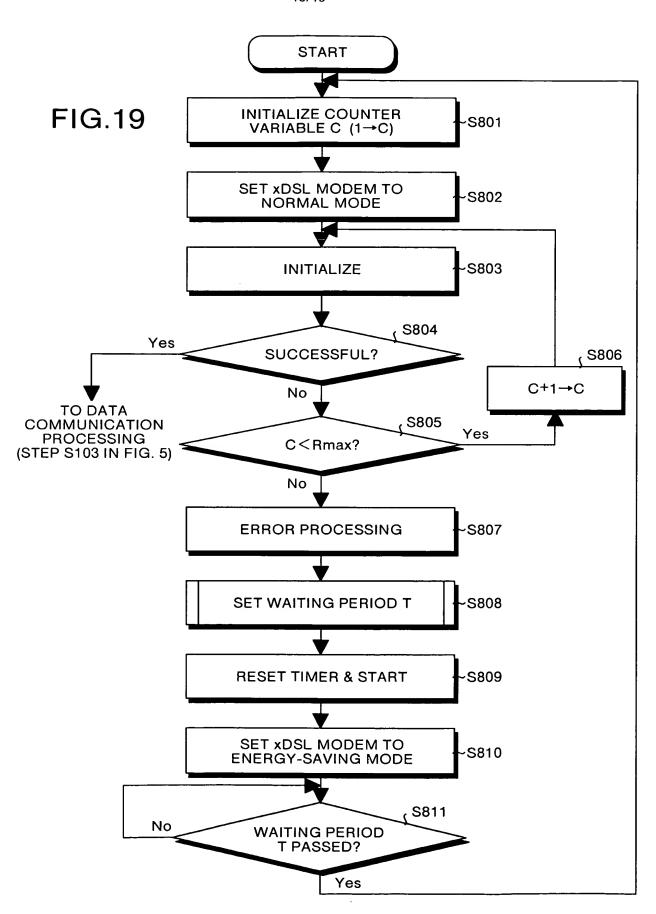


FIG.20

